University of Calcutta
Department of Electronic Science
University College of Science
Rashbehari Shiksha Prangan
92, A. P. C. Road, Kolkata – 700 009

Advertisement for Admission to Ph.D. (Science) Program (RET) in the Department of Electronic Science February, 2019

Applications are invited for admission to the Ph.D. (Science) Program in the Department of Electronic Science. Candidates willing to pursue the research work leading subsequently to Ph.D. (Science) degree in Electronic Science of the University of Calcutta should apply in the prescribed proforma downloadable from University website (www.caluniv.ac.in). The duly filled in application form along with the receipt of deposition of application fee of `100/- only in **cash** to be deposited in any cash counter of the University by **challan** and the self-certified copy of relevant mark-sheets/certificates, is to be submitted to the office of the Head of the Department of Electronic Science at the above address.

The eligible candidate will have to appear at a written examination to be held in the Department, the details of which are appended below. Successful candidates will have to appear for an interview. The final selection for joining the Ph.D. (Science) Program of this Department, along with the allocation of supervisor, will be made on the basis of this interview.

Candidates who have already cleared UGC-CSIR (JRF) NET/GATE/SET/Teacher Fellowship or passed the M.Phil./M.Tech./equivalent examination are exempted from appearing for the written test but have to directly appear at the interview.

Eligibility: M. Sc. in Electronic Science / Physics; M.Tech. / M.E. in Radiophysics and Electronics / Electronics and Communication Engg**.**

The details about the admission to the proposed program are given below:

- Last date for Submission of Application: **13.03.2019**

- Date of publication of the list of candidates eligible for written test: **18.03.2019**

- Date of Admission Test (Written): **25.03.2019 at 1:00 PM**

- Full Marks for Written Test: **150** and Question Pattern: **Multiple Choice Questions (Qualifying Marks: 75 for general candidates; 67.5 for OBC candidates and 56.25 for SC/ST candidates)**.

- Date of publication of the list of successful candidates eligible for interview: **01.04.2019 (result will be displayed on the notice board of the Department)**.

- Date of Interview: **04.04.2019 from 12:00 noon onwards**

- The list of successful candidates **will be displayed on the notice board of the Department**.

- Venue of Admission Test and Interview: Department of Electronic Science, Meghnad Saha Bhavan, 92, A. P. C. Road, Kolkata – 700 009.

- Maximum No. of Ph.D. Students to be admitted (subject to the above admission criteria): **15 Reservation norms will be followed as per West Bengal State Higher Educational Institutions (Reservation in Admission) Rules**.

- Syllabus: Topics covered in B.Sc. (Honours) and M.Sc. courses in Electronic Science with good emphasis on Electronics and Physics.

The list of successful candidates called for interview on the basis of entrance test and other exemption criteria and that of finally selected candidates will be displayed in the Department notice board. **No separate call letter will be issued and no TA/DA is admissible for appearing at the test/interview**. **There is no provision for the departmental scholarship for pursuing the research work**.